

**IN THE ABSTRACT OF THE DISCLOSURE:**

Please amend the abstract as follows:

**ABSTRACT OF THE DISCLOSURE**

~~The present invention provides a~~ A display device is provided with the gate signal lines and ~~the gate electrodes of the thin film transistors which can~~ are constructed to prevent the generation of a hillock and can reduce in which the resistance is reduced in spite of having ~~the a~~ a simple structure. ~~In a~~ The display device ~~having thin film transistors on a substrate thereof, the display device includes~~ gate patterns in each of which a gate line and a gate electrode of the thin film transistor are integrally formed, the gate pattern is constituted by at least three-layered films consisting of a lowermost layer, an intermediate layer formed of at least one layer and an uppermost layer at least at either a portion of the thin film transistor or a portion of the gate pattern which crosses a drain line, and end portions of the intermediate layer are ~~retracted~~ recessed from end portions of the uppermost layer and end portions of the lowermost layer.